



## REPORT ON

### 2 -DAYS WORKSHOP ON PCB DESIGN & MANUFACTURING

**Organized by ISTE Students Chapter (MH-316) in collaboration with Department Students Club,  
Dept. Of Electronics Engineering (VLSI D&T)**

**Date:** 10/10/2025 & 11/10/2025

**Time:** 11:00 AM to 05:15 PM

**Venue:** Smart Classroom (A-206), 2<sup>nd</sup> Floor, A Building, CSMSS Chh. Shahu College of Engineering,  
Chhatrapati Sambhajnagar (M.S)- 431002

**Goal:** To provide hands-on training and practical knowledge of designing and manufacturing Printed Circuit Boards (PCBs), enabling students to bridge the gap between theoretical electronics concepts and real-world hardware implementation.

**Objective:** The main objectives of the workshop were as follows:

1. To introduce students to the fundamentals and importance of PCB design in electronic systems.
2. To provide step-by-step training on PCB design software tools and schematic creation.
3. To familiarize students with PCB layout rules, routing techniques, and design optimization.
4. To give practical exposure to PCB manufacturing processes and workflows.
5. To enhance students' technical competency and readiness for industry-level electronic design projects.

**Benefits to Students:** Following mentioned are some benefits that students got through this program.

1. Gained practical experience in PCB design tools and schematic drawing.
2. Understood the complete workflow from circuit design to physical PCB manufacturing.
3. Developed skills in component selection, routing, and layout optimization.
4. Improved employability in VLSI, electronics design, and embedded system industries.
5. Received a certificate of participation, adding value to their academic and professional profile.

**Outcomes of the Workshop: -**

1. Students learned end-to-end PCB design and manufacturing workflow.
2. Participants gained hands-on experience in schematic creation, routing, and layout optimization.
3. Improved understanding of design rules, component selection, and error checking techniques.
4. Students were motivated to design and fabricate their own PCB-based mini projects.

**About the Event:**

The 2 Days Workshop was conducted on "**PCB Design & Manufacturing**" at Department of Electronics Engineering (VLSI D&T), CSMSS Chh. Shahu College of Engineering organized by ISTE Students Chapter (MH-316) in collaboration with Department Students Club on 10<sup>th</sup> & 11<sup>th</sup> October 2025 (Friday & Saturday).

**Day 1 (Friday, 10<sup>th</sup> October 2025): -**

1. The workshop began with the arrival of guests, followed by Saraswati Poojan and lamp lighting ceremony.
2. The dignitaries present were the Speaker Guest Mr. Pawan G. Alhat, Principal Dr. G. B. Dongre, Vice-Principal Dr. D. L. Bhuyar, HOD (EE-VLSI D&T) Dr. Shrikant J. Honade and HoD (ECE-ACT) Dr.



**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@esmssengg.org](mailto:principal@esmssengg.org) Website : [www.csmssengg.org](http://www.csmssengg.org)



- Syed Sumera Ali & all faculty members of Dept. of EE(VLSI D&T).
- The program included the introduction and felicitation of the guest and addresses by the HOD (EE-VLSI D&T) Dr. Shrikant J. Honade and Principal Dr. G. B. Dongre.
  - The expert speaker, Mr. Pawan G. Alhat, Senior Project Engineer from NIELIT Aurangabad, initiated the technical sessions.
  - The first day covered sessions on:
    - Fundamentals of PCB Design and Schematic Creation
    - Component Selection and Schematic Drawing
    - Electrical Connections and Netlist Generation
  - A hands-on practice session was conducted to give students practical exposure to the software environment.

**Day 2 (Saturday, 11<sup>th</sup> October 2025):-**

- The second day began with a recap and practice session based on the previous day's learning.
- Sessions conducted included:
  - PCB Layout Rules and Placement
  - Routing and Design Optimization
  - Finalizing PCB Design
  - PCB Manufacturing Workflow
- The resource person demonstrated the complete process of moving from design to manufacturing of a PCB.
- Prof. Ganesh G. Patil, Mr. R. M. Chudiwal, Event Coordinator and Mr. T. A. Mohije, Mr. P. N. Kathar and All Faculty members of EE (VLSI D & T) department coordinated the workshop.
- The workshop concluded with a valedictory session in the presence of the Principal, Vice Principal, HODs, faculty members, and student coordinators.
- Dr. Shrikant J. Honade, HoD EE (VLSI D & T) delivered the vote of thanks, expressing gratitude to the Principal for their support, the Resource person from NIELIT Institute for their invaluable contributions, the organizing team for their hard work, and the participants for their active engagement. The HOD emphasized the significance of continued learning and innovation in the field of PCB.
- Certificates of participation were distributed to all attendees on successful completion of the two-day workshop.
- Around 80 students were present and benefitted from the workshop.

The workshop successfully achieved its objectives by providing students with practical knowledge and industrial exposure to PCB design and manufacturing processes. The department extends sincere gratitude to the Management, Principal, and Resource Person for their continuous support and encouragement in organizing such skill-oriented technical events.



**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Email :shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org



**\* INVITATION OF THE EVENT \***



**CSMSS**



**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Rd, Chhatrapati Sambhajinagar, Maharashtra-(431 002)

**Department of Electronics Engineering  
(VLSI Design & Technology)**

*You are Cordially Invited for -*

**2 day's Workshop on**

**"Printed Circuit Board (PCB)  
Design & Manufacturing"**

Organized by -

**Department Students Club**

[Electronics Skill Development-ESD & Coding Club]

*In Association with*

**ISTE Students Chapter (MH-316)**

**SPEAKER**

**Mr. Pawan G. Alhat**  
*Senior Project Engineer  
NIELIT, Aurangabad*

**IN PRESENCE OF**

**Dr. Ganesh B. Dongre**  
*Principal  
CSMSS CSCOE*

**Dr. Devendra L. Bhuyar**  
*Vice-Principal  
CSMSS CSCOE*

&

**All Department Heads**

**CONVENER**

**Dr. Shrikant J. Honade**  
*Head of Department  
Dept. of EE (VLSI D&T)*

**FACULTY CO-ORDINATORS**

**Prof. Ganesh Patil**  
*Faculty Co-Ordinator  
Dept. of EE(VLSI D&T)*

**Prof. Rohit Chudiwal**  
*Faculty Co-Ordinator  
Dept. of EE(VLSI D&T)*

**STUDENT CO-ORDINATORS**

**Ms. Rutuja Jagtap**  
*President  
Department Students Club  
Dept. of EE (VLSI D&T)*

**Ms. Rajnandini Patil**  
*Vice-President  
Department Students Club  
Dept. of EE (VLSI D&T)*

**Mr. Prasad Desale**  
*Vice-President  
Department Students Club  
Dept. of EE (VLSI D&T)*

**Date : 10th & 11th  
October 2025**

**Time : 11 AM to 5 PM**

Venue

Smart Classroom (A-206), A-Building  
CSMSS

Chh. Shahu College of Engineering



**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Email :shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org



**CSMSS**



**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi , Paithan Rd, Chhatrapati Sambhajinagar - 431002

**DEPARTMENT OF ELECTRONIC ENGINEERING**

**(VLSI DESIGN & TECHNOLOGY)**

**Organizing**

Two Days workshop on

**Printed Circuit Board (PCB) Design &  
Manufacturing**

in association with

**ISTE Students Chapter(MH-316) &**

**DEPARTMENT STUDENT CLUB**

**(ELECTRONICS SKILL DEVELOPMENT-ESD & CODING CLUB)**

**Expert / Speaker**



**Mr. Pawan G. Alhat**  
Senior Project Engineer  
NIELIT Aurangabad

**DAY 1**

- Introduction & Setup
- Component Selection & Schematic Drawing
- Electrical connections & Netlist
- Moving from schematic to PCB Layout

- PCB Layout Rules & Placement
- Routing & Design Optimization
- Finalizing PCB Design
- PCB Manufacturing Workflow

**DAY 2**

**CERTIFICATE WILL BE PROVIDED ON SUCCESSFUL  
COMPLETION OF WORKSHOP**

**Date**  
**10/10/2025 to**  
**11/10/2025**

**Time**  
**10:00 AM to 04:30PM**

**Venue**  
**Smart Classroom**  
**(A 206),**  
**CSMSS CSCOE**

**CONTACT: RUTUJA JAGTAP [ PRESIDENT ] :- 9923517947**

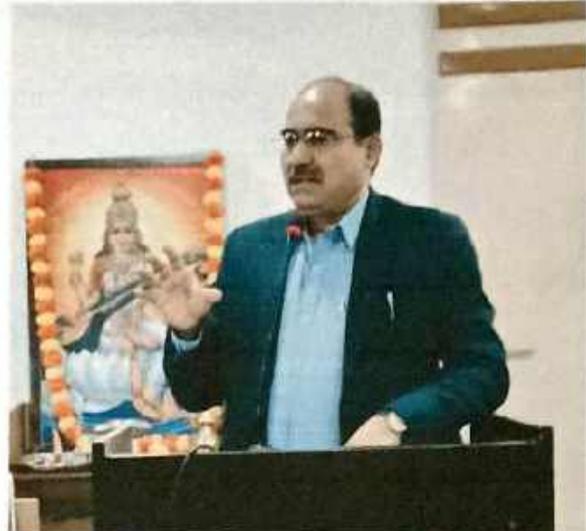
**ADITYA SHINDE [ EVENT MANAGER ] :- 7378535499**



**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@esmssengg.org](mailto:principal@esmssengg.org) Website : [www.esmssengg.org](http://www.esmssengg.org)

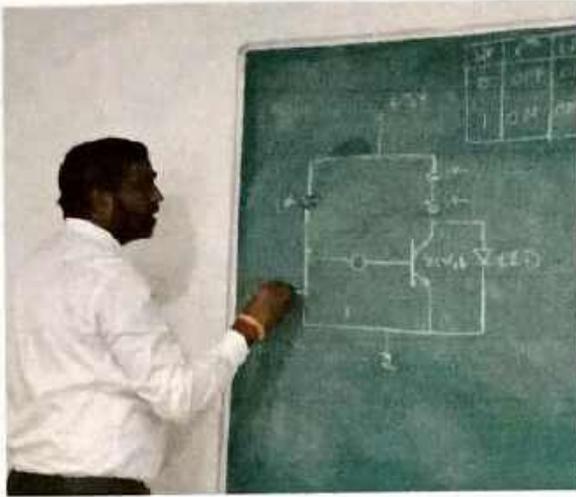




**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

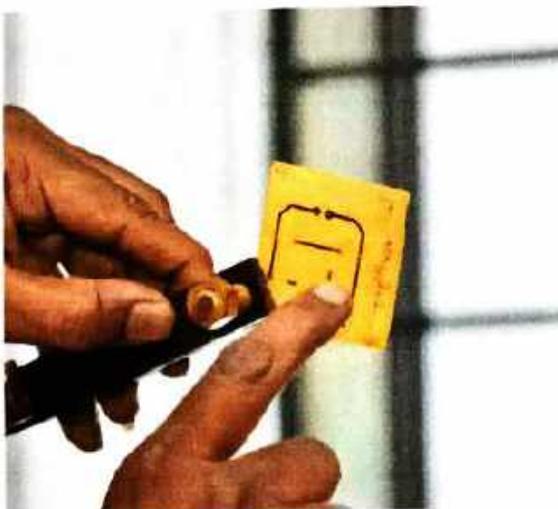
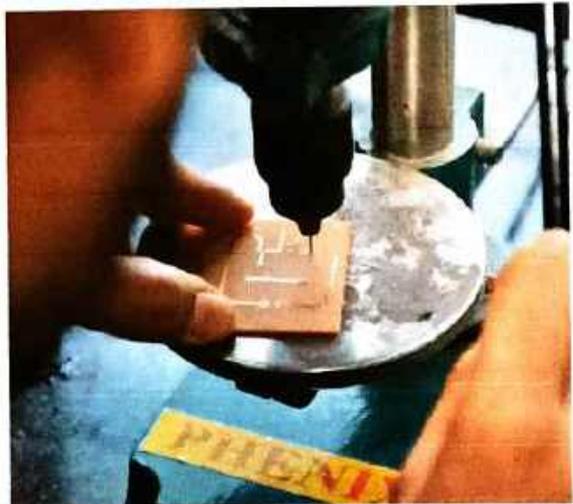
Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@csmsseengg.org](mailto:principal@csmsseengg.org) Website : [www.csmsseengg.org](http://www.csmsseengg.org)





CSMSS  
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@csmsseengg.org](mailto:principal@csmsseengg.org) Website : [www.csmsseengg.org](http://www.csmsseengg.org)

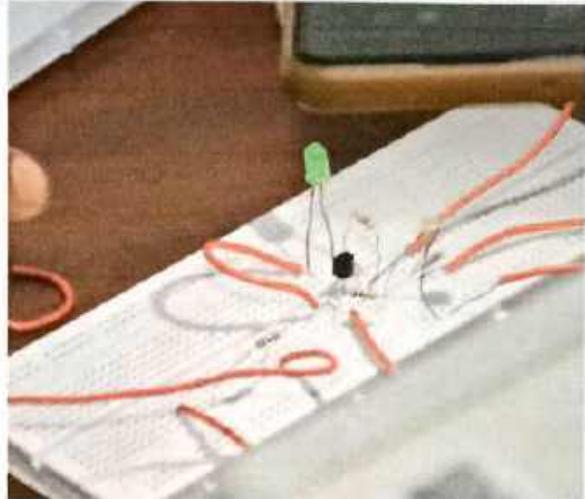
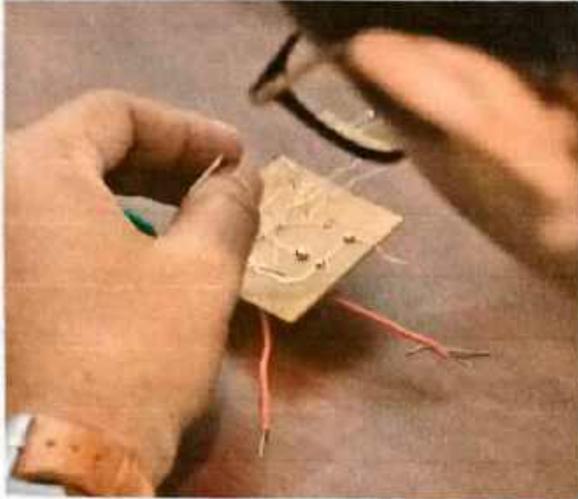




**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@csmsengg.org](mailto:principal@csmsengg.org) Website : [www.csmsengg.org](http://www.csmsengg.org)





**CSMSS**

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@esmssengg.org](mailto:principal@esmssengg.org) Website : [www.esmssengg.org](http://www.esmssengg.org)



**Prof. Ganesh G. Patil**  
Event Coordinator &  
Dept. Club Co-ordinator  
**Prof. R. M. Chudiwal**  
Event Coordinator

**Dr. Shrikant J. Honade,**  
HoD, EE (VLSI D & T)  
& ISTE Coordinator  
CSMSS CSCOE

**Dr. Ganesh B. Dongre**  
Principal  
CSMSS CSCOE



**CSMSS**  
**CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S**  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Email : [shahuengg@gmail.com](mailto:shahuengg@gmail.com), [principal@csmsseengg.org](mailto:principal@csmsseengg.org) Website : [www.csmsseengg.org](http://www.csmsseengg.org)



Date: 08/10/2025

**PCB DESIGN & MANUFACTURING WORKSHOP**

**Program Flow**

Sr. No.	Day & Date	Particulars	Attendees	Time
1	Friday 10/10/2025	Arrival	Arrival of Guest	11:00AM to 11:05AM
2		Saraswati Poojan and Lighting of Lamp	Guest, Dr. G. B. Dongre Principal Dr. D. L. Bhuyar Vice-Principal, Dr. S. J. Honade, HoD EE (VLSI D & T) and All HoD's	11:05AM to 11:08AM
3		Introduction of Guest	Prof. K. B. Dandge	11:08AM to 11:10AM
4		Felicitation of Guest	Guest felicitated by Dr. G. B. Dongre, Principal	11:10AM to 11:12AM
5		About the program	Dr. S. J. Honade, HoD EE (VLSI D & T)	11:12AM to 11:15AM
6		Address by Principal	Dr. G. B. Dongre, Principal	11:15AM to 11:18AM
7		PCB Design Fundamentals & Schematic Creation	Mr. Pawan G. Alhat Senior Project Engineer	11:20AM to 12:20PM
8			Lunch Period	12:20PM to 1:00PM
9		Component Selection & Schematic Drawing	Mr. Pawan G. Alhat Senior Project Engineer	1:00PM to 3:00PM
10			Tea Period	3:00PM to 3:15PM
11		Electrical Connections & Netlist	Mr. Pawan G. Alhat Senior Project Engineer	3:15PM to 4:15PM
12			Practice session	4:15PM to 5:15PM
13		Practice session	10:00AM to 10:30AM	
14	Saturday 11/10/2025	PCB Layout, Routing & Manufacturing Process: PCB Layout Rules & Placement	Mr. Pawan G. Alhat Senior Project Engineer	10:30AM to 12:15PM
15			Lunch Period	12:15PM to 1:00PM
16		Routing & Design Optimization	Mr. Pawan G. Alhat Senior Project Engineer	1:00PM to 2:00PM
17		Finalizing PCB Design	Mr. Pawan G. Alhat Senior Project Engineer	2:00PM to 3:00PM
18			Tea Period	3:00PM to 3:15PM
19		PCB Manufacturing Workflow	Mr. Pawan G. Alhat Senior Project Engineer	3:15PM to 4:15PM
20		Valedictory Session	Guest, Dr. G. B. Dongre Principal Dr. D. L. Bhuyar Vice-Principal, Dr. S. J. Honade, HoD EE (VLSI D & T) and All Faculty Member	4:15PM to 5:15PM



**CSMSS**  
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

**Department of Electronics Engineering (VLSI Design and Technology)**

**Attendance Report**

<b>Name of event: PCB Design and Manufacturing Workshop</b>	
<b>Class: TY(ECE)</b>	<b>Date: 10/10/25</b>
<b>Session No.: 1 - I</b>	<b>Timing: Hour 3</b>

**List of Participants**

Electronics & computer engineering

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	EC3132	Dipali Radhakisan Gund	ECE	
2	EC3143	Arti Babasaheb Chhadiday	ECE	
3	EC3165	Aditi Deepak Sawlhedkar	ECE	
4	EC3173	Tejasvini Dnyaneshwar ubale	ECE	
5	EC3157	Sayali Bhausaheb Chavan	ECE	
6	EC3107	Shruti Ganesh sangam	ECE	
7	EC3104	Nikita Pandurang Meghale	ECE	
8	EC3129	Vaishnavi Vitthal Bhutekar	ECE	
9	EC3141	Pooja Abasaheb chondhe	ECE	
10	EC2118	Shreyash Dagguram Waghmare	ECE	
11	EC3145	Harshad Madhav chappe	ECE	
12	EC3169	Pradrep Prjaneswar kalc	ECE	
13	EC3138	Nikita Pradrep Naik	ECE	
14	EC3162	Vaishnavi Ganesh mohite	ECE	
15				
16	AC3103	Bahure Yurraj Shivasing	ACT	
17	AC3131	Nitin Arun Kulk	ACT	
18	AC3165	Prachi Dattu Patil	ACT	
19	AC3169	Pooja M. shikave	ACT	
20	AC3166	Prati. N. Gorad	ACT	
21	ACT3162	Nikita B. kuhile	ACT	
22	ACT3153	Sayali S. Herne	ACT	



CSMSS  
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	AE3170	Shrawani Bhaskar Dhakne	ACT	Shrawani
24	AC3154	Yash Ingle	ACT	Yash
25	AC3164	Krushna Natwade	ACT	Krushna
26				
27				
28				
29				
30				
31				
32				
33				
34				
35				
36				
37				
38				
39				
40				
41				
42				
43				
44				
45				
46				
47				
48				
49				
50				
51				
52				

24 Present



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

# CHH. SHAHU COLLEGE OF ENGINEERING

Kanehanwadi, Patihan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.

Email: shahueng@gmail.com, Website : www.csmsseng.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.

DTE Code: 2533

## Department of Electronics Engineering (VLSI Design and Technology)

### Attendance Report ECE

Name of event: PCB Design and Manufacturing Workshop	
Class: TY (ECE)	Date: 10-10-25
Session No.: 1-IT	Timing: 3 to 5 PM

### List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	EC3165	Aditi Deepak Sawkhedkar	ECE	Aditi
2	EC3173	Tejasvini Dnyaneshwar Ubale	ECE	
3	EC3143	Ayvi Babasaheb Chhadidar	ECE	
4	EC3132	Dipali Radhakisan Gund.	ECE	
5	EC3107	Shruti Ganesh Sangam	ECE	
6	EC3104	Nirita pandurang Meghale	ECE	
7	EC3129	Vaishnavi nirnal Bhutekar	ECE	
8	EC3141	Pooja Abasaheb Chudle	ECE	
9	EC3118	Shreyash Dnyanram Waghmare	ECE	
10	EC3145	Haashak Madhav Dhurpe	ECE	
11	EC3169	Pradep Dnyanashoor Kale	ECE	
12	EC3157	Sayali Bhausaheb Chavan	ECE	
13	EC3138	Nikita Pradep Naik.	ECE	
14	EC3162	Vasshavi Ganesh Mohite	ECE	
15				
16	ACT	3:00 to 5:00		
17	AC3165	Prachi Datta Patil	ACT	
18	AC3131	Nitin Asun Kolo	ACT	
19	AC3103	Yuvraj Shreshing Babure	AIT	
20	AC3154	Yash Ninajy Ingle	ACT	
21	AC3164	Nalawade Kushna Ekhat	ACT	
22	AC3162	Nikita B. Kahire	ACT	



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	AC3166	Garad Priti Namdev	ACT	<i>Garad</i>
24	AC3153	Sayali Santosh Harne	ACT	<i>Sayali</i>
25	AC3169	Pooja M. Shikhare	ACT	<i>Pooja</i>
26	AC3170	Shravani Phakre	ACT	<i>Shravani</i>
27				
28				
29				
30				
31				
32				
33				
34				
35				
36				
37				
38				
39				
40				
41				
42				
43				
44				
45				
46				
47				
48				
49				
50				
51				
52				

24 Present



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)

Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.

Email : shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Department of Electronics Engineering (VLSI Design and Technology)

Attendance Report

Name of event: PCB Design and Manufacturing Workshop	
Class: ECE and ACT	Date: 11/10/25
Session No.: 2 - I	Timing: 10 to 12

List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
x	1	Arti Babasaheb Chhadidar ✓	ECE	
x	2	Dipali Radhakisan Gund ✓	ECE	
x	3	Tejasvini Dnyaneshwar ubale ✓	ECE	
x	4	Nikita Pradeep Naik ✓	ECE	
x	5	Vaishnavi Ganesh Mohite ✓	ECE	
x	6	Sayali Bhausaheb Chavan ✓	ECE	
x	7	Shruti Ganesh Sangam ✓	ECE	
x	8	Nikita Pandurang Meghale ✓	ECE	
x	9	Pooja Abasaheb chondke ✓	ECE	
x	10	Vaishnavi Vikthal Bhutekar ✓	ECE	
x	11	Sagar Ramkisan Jusundare ✓	ECE	
x	12	Pradeep Dnyaneshwar Kale ✓	ECE	
x	13	Krushna Ashok Gondge ✓	ECE	
✓	14	Harshad Madhav Dhuppe ✓	ECE	
✓	15	Shreyash Dajduram Wajhmaje ✓	FCB	
	16	ACT		
x	17	Prachi Dattu Patil	ACT	
x	18	Nitin Arun Kale	ACT	
x	19	Yurraj Shirsing Bahure	ACT	
x	20	Yash Ninaji Ingle	ACT	
x	21	Nalawade krushna Eknoth	ACT	
x	22	Harna Sayali Santosh	ACT	



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
X 23	ACT3162	Nikita Babasaheb Kuhile	ACT	(N)Kuhile
X 24	ACT3166	Prithi Namdev Garad	ACT	Prithi
X 25	ACT3169	Pooja Mahadev Shikare	ACT	Pooja
X 26	AC 3170	Shravani Bhaskar Dhakne	ACT	Shravani
27				
28				
29				
30				
31				
32				
33				
34				
35				
36				
37				
38				
39				
40				
41				
42				
43				
44				
45				
46				
47				
48				
49				
50				
51				
52				



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Department of Electronics Engineering (VLSI Design and Technology)

Attendance Report

Name of event: PCB Design and Manufacturing Workshop	
Class: ECE and ACT	Date: 11/10/25
Session No.: 2-II	Timing: 1 to 3

List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	3173	Tejasvini Dnyaneshwar ubale	ECE	
2	EC3143	Arti Babasaheb Chhudidar	ECE	
3	EC3132	Dipali Radhakisan Gund	ECE	
4	EC3107	Shruti Ganesh Sangam	ECE	
5	EC3104	Nikita pandurang Meghale	ECE	
6	EC3157	Sayali Bhausaheb Charan	ECE	
7	EC3138	Nikita Pradeep Naik	ECE	
8	EC3162	Vaishnavi <sup>Pinesh</sup> Mohite	ECE	
9	EC3152	Sagar Ramkisan Susundre	ECE	
10	EC3169	Pradeep Dnyaneshwar kale	ECE	
11	EC3145	Harshad Madhav Dhuppe	ECE	
12	EC3118	Shreyash Jaydram Waghmare	ECE	
13	EC3129	vaishnavi vithal Bhatelwar	ECE	
14	EC3141	Pooja Abasaheb chondke	ECE	
15				
16	AC3164	Nalawade Krishna Eknath	ACT	
17	AC3154	Yash Ninaji Ingle	ACT	
18	AC3103	Yuvraj Shivsing Bahure	ACT	
19	AC3131	Nitin Ason Kale	ACT	
20	AC3165	Parachi Dattu Patil	ACT	
21	AC3170	shruvani Bhatkar Dhakre	ACT	
22	AC3166	Priti Namdev Garad	ACT	



# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	ACT3162	Nikita Babasaheb kuhile	ACT	
24	ACT3153	Sayali santosh Harne	ACT	
25				
26				
27				
28				
29				
30				
31				
32				
33				
34				
35				
36				
37				
38				
39				
40				
41				
42				
43				
44				
45				
46				
47				
48				
49				
50				
51				
52				



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Department of Electronics Engineering (VLSI Design and Technology)

Attendance Report

Name of event: PCB Design and Manufacturing Workshop	
Class:	Date: 13-10-25
Session No.:	Timing: 10:00 to 12:00

List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	EE3132	Dipali Radhakisan Gund	ECE	
2	EC3143	Arti Babasaheb Chhadidav	ECE	
3	EC3157	Sayali Bhausabeb Charan	ECE	
4	EC3162	Vaishnavi Ganes Mohite	ECE	
5	EC3107	Shruti Ganes sangam	ECE	
6	EC3104	Meghate Nikita Pandurang	ECE	
7	EC3129	vaishnavi vithal Bhutekar	ECE	
8	EC3141	Poojai Abasaheb Chondhe	ECE	
9	EC3118	Shreyash Jagduran Waghmare	ECE	
10	EC3145	Harshad madhav onuppe	ECE	
11	EC3152	Sagar Ramkisan Subundre	ECE	
12	EC3169	Pradcep Dnyanshwar kale	ECE	
13		ACT		
14	AC3103	Yuvraj Shivasing Bahare	ACT	
15	AC3154	Yash Ninceji Ingle	ACT	
16	AC3164	Nalawode krushnd Eknath	ACT	
17	AC3162	Nikita Babasaheb kuhile	ACT	
18	AC3153	Sayali Santosh Harne	ACT	
19	AC3166	Priti .N. Garod	ACT	
20	AC3169	Pooja M. Shikare	ACT	
21	AC3170	Shravanani B. Dhakne	ACT	
22	AC3165	Prachi Patil	ACT	



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)

Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.

Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Department of Electronics Engineering (VLSI Design and Technology)

Attendance Report

Name of event: PCB Design and Manufacturing Workshop	
Class:	Date: 13/10/2028
Session No.:	Timing: 1 to 3 pm

List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	3154	Vedashree Gavali	EE(VLSI)	
2	3156	Purnjal Y. Kapkan	EE(VLSI)	
3	3161	Rutuja Makarjiye	EE(VLSI)	
4	3120	Prerna Selve	EE(VLSI)	
5	3104	Radhika Thorke	EE(VLSI)	
6	3136	Devika Bodkhe	EE(VLSI)	
7	3135	Vaishnavi Wadekar	EE(VLSI)	
8	3105	Safna Sonawane	EE(VLSI)	
9	3107	Aarya Kulkarni	EE(VLSI)	
10	3115	Mrunmai Bhasale	EE(VLSI)	
11	3113	Shital K. Anhad	EE(VLSI)	
12	3125	Nandini P. Tadhan	EE(VLSI)	
13	3131	Nikita Nikam	EE(VLSI)	
14	3122	Sheetal Adhane	EE(VLSI)	
15	3127	Sakshi Perse	EE(VLSI)	
16	3129	Monali Tayade	EE(VLSI)	
17	3148	Sakshi Natkar	EE(VLSI)	
18	3139	Shejal Kharad	EE(VLSI)	
19	3128	Akshays Magar	EE(VLSI)	
20	3101	Saarnil S. Shinde	EE(VLSI)	
21	3166	Rutuja Vithal Teke	EE(VLSI)	
22	3150	Sakshi Ambhore	EE(VLSI)	



# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email : shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	3146	yogita N. sonawane	EE(VLSI)	
24	3163	NAVALE BRUSHI DNYANESHWAR	EE(VLSI)	
25	8157	Khairi Gayatri santosh	EE(VLSI)	
26	3143	Natgis Munir Shaikh	EE(VLSI)	
27	3114	samruddhi baban motte	EE(VLSI)	
28	3144	Nisha Kadam	EE(VLSI)	
29	3103	Raj Ramteke	EE(VLSI)	
30	3109	AJOK Adik	EE(VLSI)	
31	3118	<del>Harsh Santosh Sonawane</del>	EE(VLSI)	
32	3138	OMKAR BHAVAN	EE(VLSI)	
33	3191	Avadhut ude.	EE(VLSI)	
34	3124	Pem Pawar	EE(VLSI)	
35	3149	virek Narayan Bambarde	EE(VLSI)	
36	3165	Gayatri sudhir shinde	EE(VLSI)	
37	3111	Aniket B. Nikam	EE(VLSI)	
38	3119	Saurabh V. Bera	EE(VLSI)	
39	3112	Vaibhav J. Kadam.	EE(VLSI)	
40	3158	Rupesh kurhade.	EE(VLSI)	
41	3160	Aniket lokhade		
42	3159	Appasaheb kusher	EE(VLSI)	
43	3162	Gaurav mene.		
44	3164	Faraz Shaikhe		
45	3155	Suraj kahate		
46	3167	Anurag thale	EE(VLSI)	
47	31	Sujal Singh Thakur.	EE(VLSI)	
48				
49				
50				
51				
52				



# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

### Department of Electronics Engineering (VLSI Design and Technology)

### Attendance Report

Name of event: PCB Design and Manufacturing Workshop	
Class: EE-VLSI(DET)	Date: 10/10/25
Session No.: 1	Timing: 10 : 3 : 00

### List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	V3161	Rutuja N. Maharije	EE(VLSI DET)	Rutuja
2	V3131	Nikita N. Nikam	EE(VLSI DET)	Nikita
3	V3158	YOH'S. Wavare	EE(VLSI DET)	Yoh's
4	V3120	Preena Sawe.	CG VLSI	Preena
5	V3136	Devika Bodkhe	-11-	Devika
6	V3104	Radhika Anun Thorakar	-11-	Radhika
7	V3137	Bhakti Sonawane.	-11-	Bhakti
8	V3134	Tanushel sheekar	-11-	Tanushel
9	V3117	Valshnavi shelke	-11-	Valshnavi
10	VL3144	Nisha Arvind Kadam.	EE[VLSI]	Nisha
11	VL3103	Raj Ramteke.	-11-	Raj
12	VL3164	Faraz Shaikh	-11-	Faraz
13	VL3105	Sapna Sonawane	-11-	Sapna
14	VL3107	Aarya Kulkarni	-11-	Aarya
15	VL3115	Mridhmai Bhosle	-11-	Mridhmai
16	VL3113	Shital K. Avhad	EE(VLSI)DET	Shital
17	VL3115	Nandinee P. Jadhav	EE(VLSI)	Nandinee
18	VL3122	Sheetal Bantosh Adhane	EE(VLSI)	Sheetal
19	VL3127	Sakshi K. Pere	EE(VLSI)	Sakshi
20	VL3129	Monali Suresh Tayade	EE(VLSI)	Monali
21	VL3153	Ashwini Sham Divekar	EE(VLSI)	Ashwini
22	VL3139	Khayad Shejal Sham	EE(VLSI)	Khayad



# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email : shahucengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	VL 3114	More Samrudahi Baban	EE(VLSI)	
24	VL 3143	Shaikh Nargis Munir	EE (VLSI)	
(25)	VL 3135	Wadkar Vaishvi S.	-11-	
26	VL 3163	Navale Srujati Dnyaneshwar	EE (VLSI)	
27	VL 3166	Teke Rutuja Vitthal	EE(VLSI)	
28	VL 3157	Gajari Khairi Gayatri Santosh	EE(VLSI)	
29	VL 3146	Sonawane Yogita Nalhusant	EE(VLSI)	
30	VL 3101	Swapnil sudhakar shinde	EE(VLSI)	
(31)	VL 3130	rushikesh anil gavhane	EE(VLSI)	
(32)	VL 3126	Sanket BhimRao Nikam	EE (VLSI)	
33	VL 3128	Akshay Sanjay Magar	EE(VLSI)	
34	VL 3165	Gayatri Sudhir Shinde	EE (VLSI)	
35	VL 3149	Vivek Narayan Bambarde	EE(VLSI)	
36	VL 3110	Aniket Sunil Nikam	EE (VLSI)	
37	VL 3119	Saurabh Vichnu Bore	EE (VLSI)	
38	VL 3155	Sunil Prameshing Khatke	EE (VLSI)	
39	VL 3159	Appasaheb Bhausaheb Kusher	EE (VLSI)	
(40)	VL 3162	Gaurav. Gajanan Mene	EE (VLSI)	
41	VL 3108	Tejas. Nannabh Thorat	EE (VLSI)	
42	VL 3141	Aadhit Santosh ude	EE (VLSI)	
(43)	VL 3147	Kiran Kailas Kalwar	EE (VLSI)	
(44)	VL 3140	Rohan Dadarao DEVKOR	EE (VLSI)	
45	VL 3160	Aniket Narayan Lokhande	EE (VLSI)	
(46)	VL 3158	Rupesh Vishwas Kurhade	EE (VLSI)	
47	VL 3112	Vaibhav Jalindar Kadam	EE (VLSI)	
48	VL 3	Sugalsingh Digambar Singh Thakur	EE (VLSI)	
49	VL 3138	OMKAR E. NITIN (MAVAN)	EE (VLSI)	
(50)	VL 3118	Vivek Santosh Sonawane	EE (VLSI)	
51	VL 3109	Alok Vijay Adik	EE (VLSI)	
52	VL 3156	Pranjal Yogesh Kapkar	EE (VLSI)	



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajanagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahucngg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
53	VL3154	Vedashri Prabhakar Gavali	EE(VLSI)	<i>Vedashri</i>
54	VL3121	Rajnandini Satish Patil	EE(VLSI)	<i>Rajnandini</i>
55				
56				
57				
58				
59				
60				
61				
62				
63				
64				
65				
66				
67				
68				
69				
70				

54

Mr. G. G. Patil  
Event Coordinator

Dr. Shrikant J. Honade  
HoD  
EE (VLSI D & T)



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

# CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Parithan Road, Chhatrapati Sambhaji Nagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.

Email: shahueng@gmail.com, Website: www.csmsseng.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

## Department of Electronics Engineering (VLSI Design and Technology)

### Attendance Report

Name of event: PCB Design and Manufacturing Workshop

Class: VLSI D&T Date: 11/10/25

Session No.: 2-I Timing: 10 to 12.p.m.

### List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	3121	Rajwandi Patil	VLSI D&T	Rajwal
2	3161	Rutuja N. Makrelinge	VLSI D&T	Rutuja
3	3120	Prerna Sawe	VLSI D&T	Prerna
4	3136	Devika Godakhe	VLSI D&T	Devika
5	3131	Nikita Nikam	VLSI D&T	Nikita
6	3168	Chah S. Hlavare	VLSI D&T	Chah
7	3156	Pranjali Y. Karkar	VLSI D&T	Pranjali
8	3144	Nisha Kadam	VLSI D&T	Nisha
9	3135	Vaishnavi Wadkekar	VLSI D&T	Vaishnavi
10	3104	Radhika Thorakar	VLSI D&T	Radhika
11	3105	Safna Sonawane	VLSI D&T	Safna
12	3107	Aanya Kulakrni	VLSI D&T	Aanya
13	3115	Mayurmai Bhosale	VLSI D&T	Mayur
14	3103	Raj Ramteke	VLSI D&T	Raj
15	3164	Farnaz Shaikh.	VLSI D&T	Farnaz
16	3154	Vedashri. P. Gavali	VLSI D&T	Vedashri
17	3117	Naishnavi Baresabeh Shelke	VLSI D&T	Naishnavi
18	3134	Tanushri Santosh Sherkar	VLSI D&T	Tanushri
19	3137	Bhakti Jagadish Sonawane	VLSI D&T	Bhakti
20	3143	Nargis Munir Shaikh	VLSI D&T	Nargis
21	3114	Santoshchi baban More	VLSI D&T	Santoshchi
22	3148	Sakshi Jaxman Nethar	VLSI D&T	Sakshi

4-2



# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email : shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	3139	Shejal sham kharad	VLSI(DAT)	Shejal
24	3113	Shital kailas AVhad	VLSI(DST)	Shital
25	3125	Nandinee P. Tadhav	VLSI(DST)	Nandinee
26	3153	Ashwini Divekar	VLSI(DST)	Ashwini
27	3122	Sheetal Adhane	VLSI(DAT)	Sheetal
28	3127	Sakshi Pere	VLSI(DRT)	Sakshipe
29	3129	Monali Tayade	VLSI(DAT)	Monali
30	3141	Avadhut Ude	VLSI(DST)	Avadhut
31	3149	Vivek Bambarde	VLSI(DRT)	Vivek
32	3165	Gayatri shinde	VLSI(DRT)	Gayatri
33	3111	Aniket b. Nikam	VLSI(DRT)	Aniket
34	3119	Saurabh v. Bore	VLSI(DRT)	Saurabh
35	3108	Tejay N. Thorat	VLSI(DRT)	Tejay
36	3155	Suraj P. Kahate	VLSI(DST)	Suraj
37	3159	Appasaheb B. Kusher	VLSI(DRT)	Appasaheb
38	3162	Garban G. Mene	VLSI(DAT)	Garban
39	3118	Vivek Santosh Sonawane	VLSI(DST)	Vivek
40	3152	pavan sunil bhise	VLSI(DST)	Pavan
41	3160	Aniket Narayan Lokhande	VLSI(DRT)	Aniket
42	3130	Rushikesh Bhanane	VLSI(DRT)	Rushikesh
43	3112	Vaibhav Jalindar Kadam	VLSI(DAT)	Vaibhav
44	3140	Rohan Dadasao Denkar	VLSI(DRT)	Rohan
45	3138	Omkar - N. Chavan	VLSI(DRT)	Omkar
46	3109	AIOK V. Adik	VLSI(DRT)	AIOK
47	3102	Aditya S. Suryavanshi	VLSI(DRT)	Aditya
48	3143	Yogita N. Sonawane	VLSI(DRT)	Yogita
49	3150	Sakshi Parmeshwar Amlehere	VLSI(DRT)	Sakshi
50	3163	Navale Shushti Dnyaneshwar	VLSI(DRT)	Navale
51	3166	Teke Rutuja Vitthal	VLSI(DRT)	Teke
52	3137	Khaira Gayatri Santosh	VLSI(DRT)	Khaira



**CSMSS**  
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Pailhan Road, Chhatrapati Sambhujinagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmsscngg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
53	3123	Rutuja.N. Jagtap	VLSJ	<del>_____</del>
54		Sujal Singh Thakur.		
55				
56				
57				
58				
59				
60				
61				
62				
63				
64				
65				
66				
67				
68				
69				
70				

Event Coordinator

**Dr. Shrikant J. Honade**  
HoD  
EE (VLSI D & T)



CSMSS

CHHATRAPATI SHAHU MAHARAJ SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Department of Electronics Engineering (VLSI Design and Technology)

Attendance Report

Name of event: PCB Design and Manufacturing Workshop	
Class: EE (VLSI D&T)	Date: 11/10/25
Session No.: 2-II	Timing: 1 to 3

List of Participants

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	3121	Rajrandini Satish Patil	EE-VLSI	Rajrandini
2	3161	Rutuja Makariye.	EE-VLSI	Rutuja
3	3168	Choti Kavare	EE-VLSI	Choti
4	3156	Pranjal Kapkar	EE-VLSI	Pranjal
5	3100	Prema Salve	EE-VLSI	Prema
6	3104	Radhika Thorke	EE-VLSI	Radhika
7	3136	Devika Bodakhe	EE-VLSI	Devika
8	3144	Nisha Kadam.	VLSI D&T	Nisha
9	3103	Raj Ranuteke	-11-	Raj
10	3164	Faraz Sheikh	-11-	Faraz
11	3105	Sofna Sonawane	VLSI D&T	Sofna
12	3107	Aarya Kulkarni	VLSI D&T	Aarya
13	3115	Mrunmai Bhasale	VLSI D&T	Mrunmai
14	3135	Vaishnavi Wadekar	VLSI D&T	Vaishnavi
15	3154	Vedashri Gavali	VLSI D&T	Vedashri
16	3134	Tanushri S. Sherkar	VLSI D&T	Tanushri
17	3137	Bhakti J. Sonawane	VLSI D&T	Bhakti
18	3117	Vaishnav B. Shelke	VLSI D&T	Vaishnav
19	3143	Natqis M. Shaikh	VLSI D&T	Natqis
20	3114	Sambuddhi B. More	-11-	Sambuddhi
21X	3139	Shejal S. Khurad	-11-	Shejal
22X	3148	Sakshi L. Natkar	VLSI D&T	Sakshi



# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSHTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	3146	Yogita Nalbhikant Sonawane	VLSI D&T	
24	3150	Sakshi Parmeshwar Amkhere	VLSI D&T	
25	8163	Navale Shubhi Dnyaneshwar	VLSI D&T	
26	3166	Teke Rutuja Vitthal	VLSI D&T	
27	3157	Khairi Gayatri Santosh	VLSI D&T	
28	3109	Alok Vijay Adik	VLSI D&T	
29	3140	Rohan Padarnao Dewkar	VLSI D&T	
30	3102	Arditya Suryawanshi	VLSI D&T	
31	3112	Vaibhavi Jalindar Kadam	VLSI D&T	
32	3119	Seemabhi Vishnu Bore	VLSI D&T	
33	3118	Vivek Santosh Sonawane	VLSI D&T	
34	3149	Vivek Narayan Bambarde	VLSI (D&T)	
35	3165	Gayatri Sudhir Shinde	VLSI (D&T)	
36		Sugalsingh Digambar Singh Thakur	VLSI (D&T)	
37	3162	Garupa Gajanan Mene	VLSI (D&T)	
38	3155	Suraj Pramesing Khatke	VLSI (D&T)	
39	3159	Appasaheb B. Kushke	VLSI (D&T)	
40	3138	Omkar -N. CHAVAN	VLSI (D&T)	
41	3130	Rushikesh A. Gadhane	VLSI (D&T)	
42	3141	Aniket . S. Ude	VLSI (D&T)	
43	3160	Aniket Lokhande	VLSI (D&T)	
44				
45				
46				
47				
48				
49				
50				
51				
52				



**CSMSS**  
CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

**Department of Electronics Engineering (VLSI Design and Technology)**

**Attendance Report**

<b>Name of event: PCB Design and Manufacturing Workshop</b>	
<b>Class: EE(VLSI D&amp;T)</b>	<b>Date: 13/10/2025</b>
<b>Session No.: 3-I</b>	<b>Timing: 10:00 to 12:00</b>

**List of Participants**

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
X	V3161	Rutuja N. Mahalinge	EE(VLSI&T)	Rutuja
X	V3104	Radhika Thorat	EE(VLSI)	Radhika
X	V3107	Aeya Kulkarni	EE(VLSI)	Aeya
X	V3120	Preeta Salve	EE(VLSI)	Preeta
X	V3136	Devika Bodkhe	EE(VLSI)	Devika
X	V3131	Nifity Nikam	EE(VLSI)	Nifity
X	V3121	Rajrandini Padi	EE(VLSI)	Rajrandini
X	V3105	Sapna Sonawane		Sapna
X	V3115	Melammai (Bluse)		Melammai
X	VL3154	Vedashri Gavali	EE(VLSI)	Vedashri
X	VL3156	Pranjal Kapkar	EE(VLSI)	Pranjal
X	VL3135	Vaishnavi Wadekar	EE(VLSI)	Vaishnavi
X	VL3144	Nisha Kadam	EE(VLSI)	Nisha
X	VL3103	Raj Ramteke	EE(VLSI)	Raj
X	VL3164	Faaz Shaikh	EE(VLSI)	Faaz
X	<del>VLSI</del> 3118	Vivek Sonawane	EE(VLSI)	Vivek
X	VL3116	Om P. Pandagale	EE(VLSI)	Om P.
X	VL3101	Swapnil Shinde	EE(VLSI)	Swapnil
X	VL31028	Akshay S. Magar	EE(VLSI)	Akshay
X	VLSI3140	Rohan D. Deokar	EE(VLSI)	Rohan
X	VLSI3130	Rishikesh Gavhane	EE(VLSI)	Rishikesh
X	VLSI3145	Nikhar Modke	EE(VLSI)	N.P. Modke

# CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

## CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
23	3149	G Vivek Narayan Bambarde	VLSI(DAT)	
24	3165	Gayatri sudhir shinde	VLSI(DAT)	
25	3106	Pranav Sunil Rode	VLSI(DAT)	
26	3106	Sanket B. Nitam	VLSI(DAT)	
27	3111	Aniket b. Nikam	VLSI(DAT)	
28	3119	Saurabh Vishnu Bore.	VLSI(DAT)	
29	3112	Vaibhav J. Kadam	VLSI(DAT)	
30	3149	Kiran K. Kalwar	VLSI(DAT)	
31	3142	Vikram Vilas Rathod	VLSI(DAT)	
32	3151	Kastha Banswal	VLSI(DAT)	
33	3158	Rupesh Vishwas Kurhade	VLSI(DAT)	
34	3108	Tejas. Thorad	VLSI(DAT)	
35	3124	Pranav Pawar	VLSI(DAT)	
36	3141	Avadhut Vad	VLSI(DAT)	
37	3152	pavan sunil Bhise	VLSI(DAT)	
38	3109	Alok Vijay Adik	VLSI(DAT)	
39	3102	Aditya Sande Suryawanshi	VLSI(DAT)	
40	3146	Yogita N. Sonawane	VLSI(DAT)	
41	3150	Sakshi Parmeshwar Ambhare	VLSI(DAT)	
42	3163	Navale Shrushti Dnyaneshwar	VLSI(DAT)	
43	3166	Teke Rutuja Vitthal	VLSI(DAT)	
44	3157	Khairi Gayatri Santosh	VLSI(DAT)	
45	3122	Bheetal Santosh Adhane	VLSI(DAT)	
46	3127	Sakshi Kakasaheb Peze	VLSI(DAT)	
47	3129	Monali Suresh Jayde	VLSI(DAT)	
48	3113	Shital Kapias Avhad	VLSI(DAT)	
49	3128	Nandinee Pandharnath Jadhav	VLSI(DAT)	
50	3133	Ashwini Sham Divakar	VLSI(DAT)	
51	3134	Vedashri Gavali	VLSI(DAT)	
52	3156	Pranjal Y. Kapkar	VLSI(DAT)	



CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S

CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 002 (M.S)

Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.

Email :shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
53	3148	Nattkar Sakshi Arman	EE(VLSI D & T)	Sakshi
54	3139	Shejal Sham Kharad	EE(VLSI D & T)	Shejal
55	3114	Samruddhi baban more	EE(VLSI D & T)	Samruddhi
56	3143	Shaikh Nargis munir	EE(VLSI D & T)	Nargis
57	3134	Tanushri Santosh Sherkar.	EE(VLSI)	Tanushri
58	3137	Bhakti Jagadish Sonawane	EE(VLSI)	Bhakti
59	3117	Vaishnavi B. Shelke	EE(VLSI)	Vaishnavi
60	3132	Akshada Gajanan Kakde	EE(VLSI)	Akshada
61	3133	Vedilca Suraj Patil	EE(VLSI)	Vedilca
62		Sugalsingh D. Thakur	EE(VLSI)	Sugalsingh
63	3138	Omkar Chavhan	EE(VLSI)	Omkar
64	3160	Aniket N. Lokhande	EE(VLSI)	Aniket
65	3159	Appasaheb B. Kushee	EE(VLSI)	Appasaheb
66	3162	Gaunav G. Mene	EE(VLSI)	Gaunav
67	3157	Sasaji. Kahate	EE(VLSI)	Sasaji
68	3167	Anusag Thale	EE(VLSI)	Anusag
69	3123	Ruhija Jagtap	EE(VLSI)	Ruhija
70				

Mr. G. G. Patil  
Event Coordinator

Dr. Shrikant J. Honade  
HoD  
EE (VLSI D & T)



**CSMSS**  
CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)  
Tel.: (0240) 2646363, 2646373, 2646350, 2379012, Fax: (0240) 2379015.  
Email : shahuengg@gmail.com, Website : www.csmssengg.org



Approved by AICTE-New Delhi, DTE-Govt. of Maharashtra, Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere.  
DTE Code: 2533

**Department of Electronics Engineering (VLSI Design and Technology)**

**Attendance Report**

<b>Name of event: PCB Design and Manufacturing Workshop</b>	
<b>Class:</b> ECE & ACT	<b>Date:</b> 13/10/25
<b>Session No.:</b> 3-II	<b>Timing:</b> 1 to 3

**List of Participants**

Sr. No.	Roll No.	Name of Student	Name of Dept.	Sign.
1	AC3165	Prachi Dattu Patil	ACT	<i>Patil</i>
2	AC3131	Nitin Arun Kale	ACT	<i>Kale</i>
3	AC3154	Yash Ninaji Ingle	EC(ACT)	<i>Yash Ingle</i>
4	AC3164	Nalawade Koushna Ekmath	EC(ACT)	<i>Koushna</i>
5	AC3162	Nikita B. Kuhile	EC(ACT)	<i>Nikita</i>
6	AC3153	Sayali S. Harnic	EC(ACT)	<i>Sayali</i>
7	AC3166	Priya N. Gorad	ACT	<i>Priya</i>
8	AC3169	Shikare pooja	ACT	<i>Pooja</i>
9	AC3170	Shrawani Dhakne	ACT	<i>Shrawani</i>
10				
11				
12				
13				
14				
15				
16				
17				
18				
19				
20				
21				
22				