



**CSMSS**

Chhatrapati Shahu Maharaj Shikshan Sanstha's  
**CHH. SHAHU COLLEGE OF ENGINEERING**

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 011  
Ph. No.: (0240) 2646373, 9922668199, 2646350 Fax: (0240) 2646222  
Website: www.csmssengg.org



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## Department of Electronics Engineering (VLSI Design & Technology)

### **Research & Development Cell (R&DC) Activity Report**

<b>Name of the event:</b>	Awareness Program on How to Write and Publish Articles in Different Conferences and Journals: Drafting a Paper in IEEE Template Format
<b>Date of the event:</b>	06 <sup>th</sup> April 2025 (FYE) and 07 <sup>th</sup> April 2025 (SYE)
<b>Organized by:</b>	Department of Electronics Engineering (VLSI Design & Technology) of CSMSS Chh. Shahu College of Engineering
<b>In Collaboration with:</b>	Research & Development Cell of CSMSS Chh. Shahu College of Engineering
<b>Under the Initiative:</b>	Research & Development Cell of CSMSS Chh. Shahu College of Engineering
<b>Speaker:</b>	Dr. Jitesh Shinde Associate Professor, CSMSS Chh. Shahu College of Engineering
<b>Target Audience:</b>	1) First-year students of Department of Electronics Engineering (VLSI Design & Technology). 2) Second-year students of Department of Electronics Engineering (VLSI Design & Technology). 3) All the faculty Members of Department of Electronics Engineering (VLSI Design & Technology).
<b>Venue / Platform / Mode:</b>	Offline Mode (Smart Classroom and A203)
<b>Coordinator:</b>	Dr. Jitesh Shinde Associate Professor, CSMSS Chh. Shahu College of Engineering
<b>Objective:</b>	<ul style="list-style-type: none"><li>To guide students on the process of writing a research article</li><li>To provide information about various conferences and journals</li><li>To explain the procedure for submitting and publishing paper</li></ul>
<b>Overview</b>	The two offline Awareness Programmes on How to Write and Publish Articles in Different Conferences and Journals: <b>Drafting a Paper in IEEE Template Format</b> were conducted in association with the R & D cell and Department of Electronics Engineering (VLSI Design & Technology) for the students of first and second year of Department of Electronics Engineering (VLSI Design & Technology) on 6th and 7th April respectively. The session aimed to guide students on the process of writing a research article, and to explain the procedure for submitting and publishing paper.
<b>Participation</b>	The students of first year and second year along with the faculties of Department of Electronics Engineering (VLSI Design & Technology) enthusiastically participated in the session, demonstrating high levels of interest and engagement.

<b>Fees:</b>	<b>No fees to attend the program. It was an Awareness Program.</b>
<b>Key Outcomes:</b>	<ul style="list-style-type: none"> <li>➤ Faculty &amp; Students gained insight on the process of writing a research article.</li> <li>➤ The session provided step-by-step guidance on the drafting a paper in IEEE Template Format</li> <li>➤ Participants became aware of significance of publication of research article in a conference or journal, google scholar, citation count, and others.</li> <li>➤ Students and Faculties expressed keen interest in applying the knowledge in coming conference organized by the college.</li> <li>➤ A Q&amp;A segment allowed students and faculties to clarify their doubts directly with experts.</li> </ul>
<b>Feedback &amp; Suggestions:</b>	Students and Faculties expressed keen interest in applying the knowledge in coming conference organized by the college.
<b>Conclusion:</b>	The session was highly informative and served as a steppingstone for first-year and second year students and faculties of the department to understand and explore the importance of publication their innovation or research work either in reputed conference or journal.



**Photo:**





**Dept. R&DC Member**

Dr. J. R. Shinde

**HoD (CS&E)**

Dr. S. J. Honade

**R&D Cell**

Dr. Y. H. Bhosale

**Principal**

Dr. G. B. Dongre